Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	56	memory and first adj3 delay and second adj delay and master with slave with register	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/15 08:51
L19	702	master with slave with clock same delay	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/15 08:59
L29	336	generate$3 adj3 master same generat$3 adj3 slave	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/15 10:48
L31	407	clock with delay with master same clock with slave	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/15 10:57
L32	455	clock with delay$3 with master same clock with slave	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/15 10:58
S2	1196	clock and delay and register with master with slave	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 15:25
S9	41	master adj3 clock with delay and slave adj3 clock with delay and (input or reference) adj clock	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 10:38
S12	92	clock and delay with register with master with slave	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:05
S15	131	external adj clock and delay with master with slave	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:06
S16	97	S15 not (S12 or S13)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:06
S17	1	2003-515569.NRAN.	DERWENT	OR	ON	2005/05/09 16:41